



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 10/757,276  
Filing Date ..... January 14, 2004  
Inventor ..... Kevin L. Beaman et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Stephen W. Smoot  
Attorney's Docket No. .... MI22-2475  
Customer No. .... 021567  
Title: Semiconductor Assemblies, Methods of Forming Structures over Semiconductor  
Substrates, and Methods of Forming Transistors Associated with Semiconductor  
Substrates

**RESPONSE TO JULY 25, 2006 OFFICE ACTION**

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Responsive to the Office Action dated July 25, 2006, Applicant amends and remarks  
as follows:

**AMENDMENTS**

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.